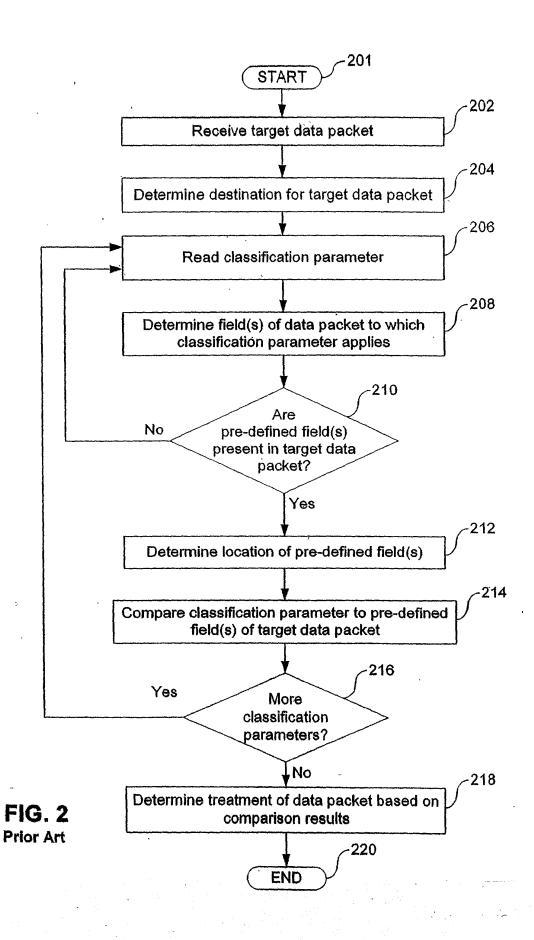
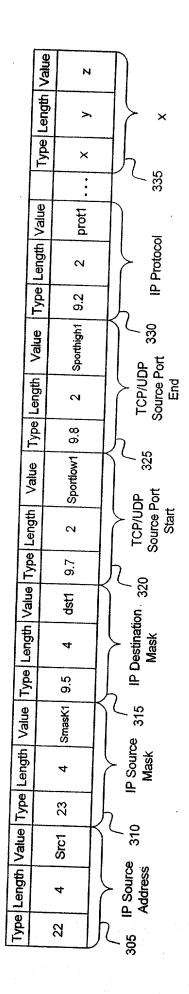


FIG. 1





302

FIG.

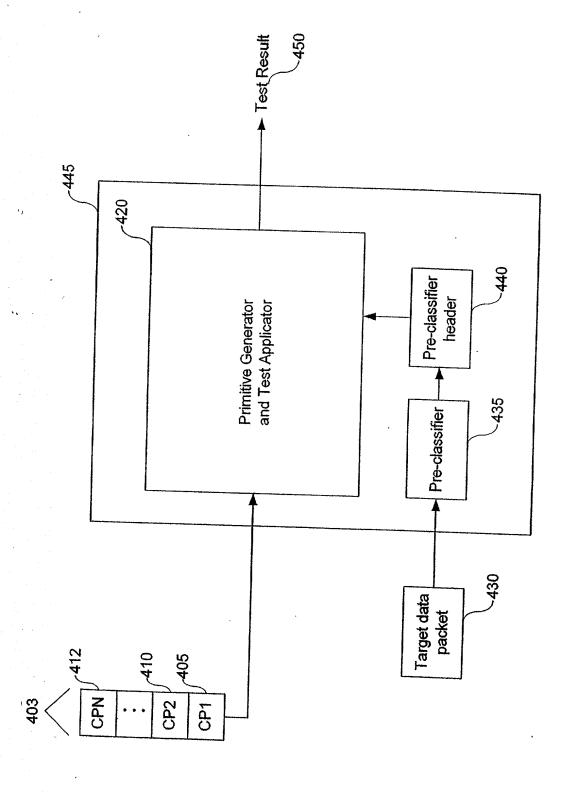


FIG. 4

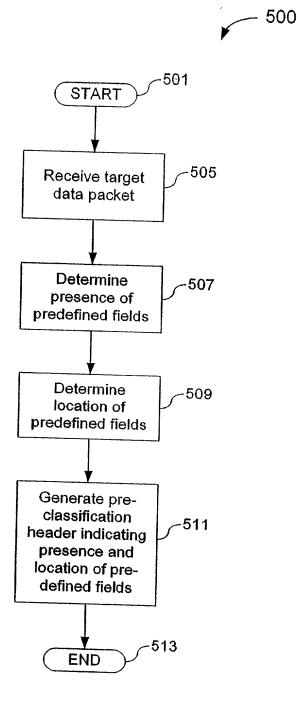
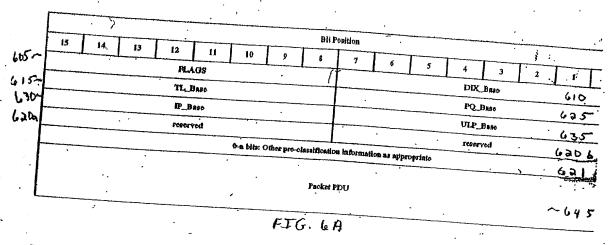


FIG. 5
Pre-classification Flowchart



7 6		Plag Bit I	osition		
uc snap.	PQ.	4 n	3 TOP	2	1 . 0
450	640	665	aro	UDP TO	reserved
	٠			# / <del>-</del>	680

FIG. 6B

## 700 A

<u> </u>	Byte	Byte		·
D. / G-	00			Byte
DA/SA	02			01
	18			10
	81		05	06
	00			
	45		08	. 00 ·
	00		00	бе
TTL/PROT/HCS			00 .	00
Src IP Addr			XX	xx
Dest IP Addr			03	04
(90 bytes)		Ub	07	08
	DA/SA  Type/Len TCI/TL V/IHL/TOS/TL ID/FL/FO TTL/PROT/HCS Src IP Addr Dest IP Addr (90 bytes)	DA/SA 00  18 Type/Len 81 TCI/TL 00 V/IHL/TOS/TL 45 ID/FL/FO 00 TTL/PROT/HCS 00 Src IP Addr 01 Dest IP Addr 05	DA/SA 00 10 02 03 18 04 Type/Len 81 00 TCI/TL 00 00 V/IHL/TOS/TL 45 00 ID/FL/FO 00 00 TTL/PROT/HCS 00 25 Src IP Addr 01 02 Dest IP Addr 05	DA/SA

FIG. 7A

700B Byte 00 Byte 10 Byte 18 Byte DA/SA 02 01 802.3 Header 03 00 10 18 04 Type/Len DSAP/SSAP/Ctl 05 06 00 802.2 LLC/SNAP 72 aa aa 10 OUI 03 00 Hdr TYPE (106 bytes) 18 08 Payload 06 00...

FIG. 7B

700C

1		Byte	Byte	Byte	Byte
		00	10	18	01
	DA/SA	02	03	00	10
802.3 Header		18	. 04	05	06
•	Type/Len	00	72		· · · · · · · · · · · · · · · · · · ·
802.2 LLC Hdr	DSAP/SSAP/Ctl	00	00	0x?3	
LLC Payload	(111 bytes)	00		·	

FF6. 7C

700 0

• *			<b>&amp;</b>	,	
		Byte	Byte	Byte	T
1	D2 / G2	00	10		Byte
802.3 Header	DA/SA	02	03	18	01
"cader		18	04	00 .	10
802.2	Type/Len	00	72	05	06
	DSAP/SSAP/Ct1	aa			
LLC/SNAP Hdr	OUI	00	aa	03	
ngr	Туре	08	10	18	
	V/IHL/TOS/TL	45	00		
	ID/FL/FO		00	00	ба
IP Header	TTL/PROT/HCS	00	.00	00	
	Gra TD 211	ď0	25	xx	00
	Src IP Addr	01	02	03	XX
IP Payload	Dest IP Addr	05	06		04
TE EGATOSG	(86 bytes)	00		. 07	08

FIG. MD



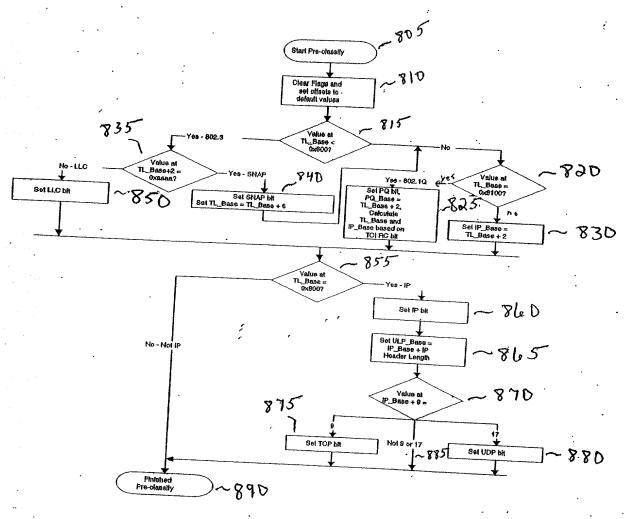


FIG. 8

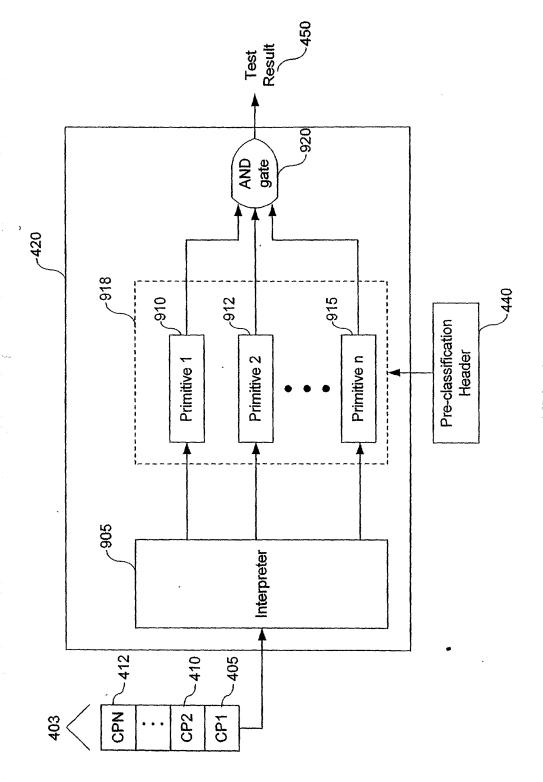


FIG. 9

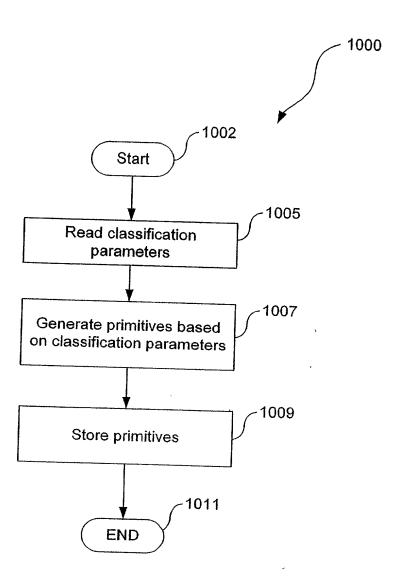


FIG. 10
Primitive Generation

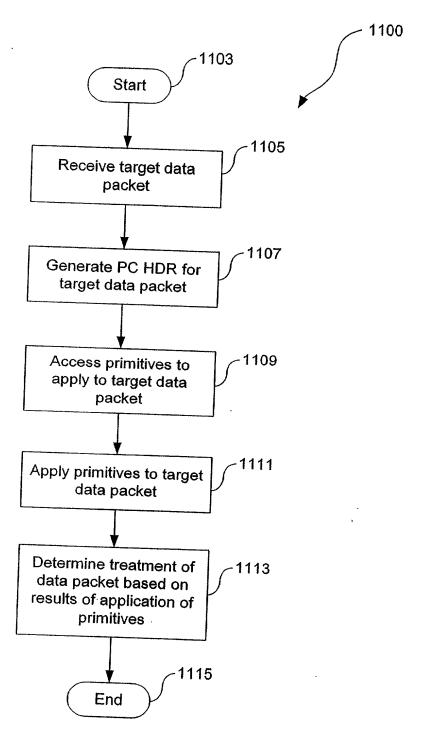


FIG. 11
Test Application

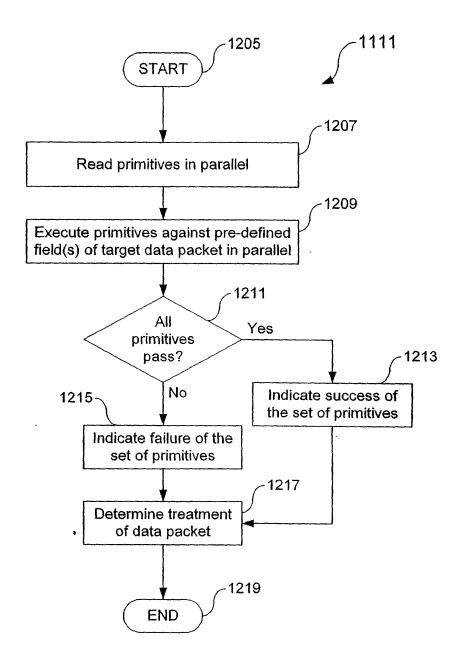


FIG. 12

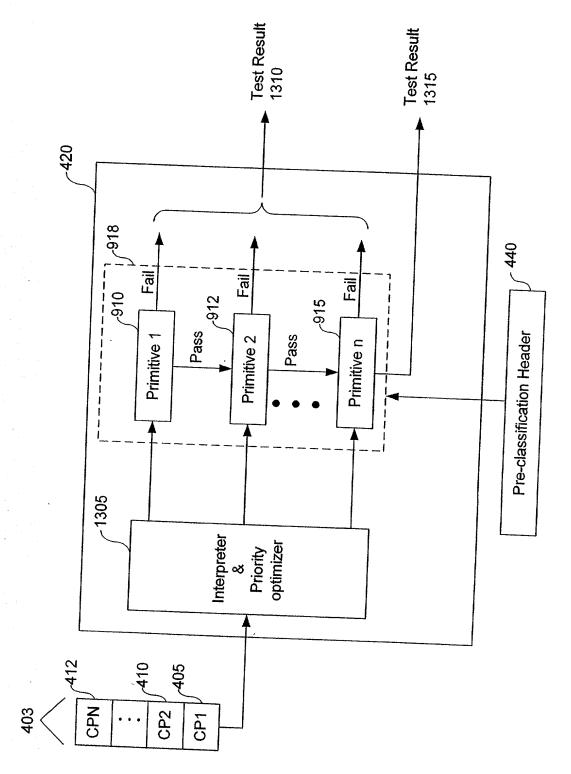


FIG. 13

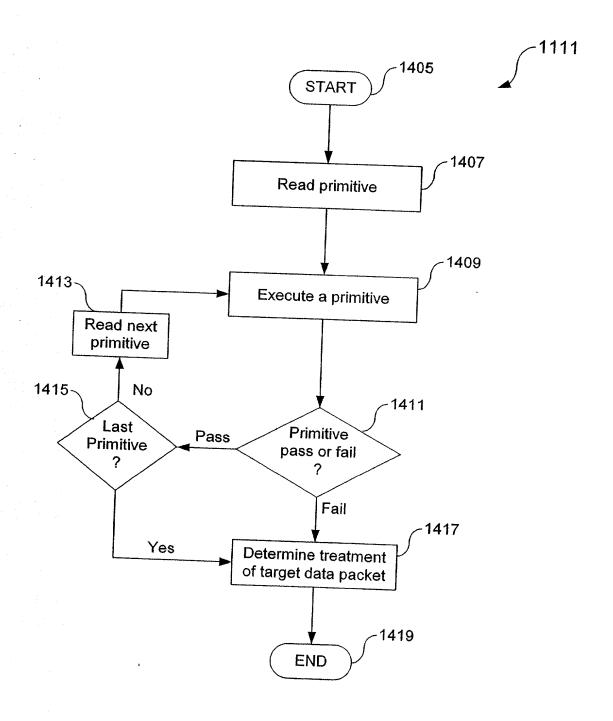


FIG. 14

			1501	•		-	
			4	•	•	`	
0	·   -			Byte	,		
OP OFF	2	3 4	5 6	7 8	9 10 11		
	TOSCI VELI	. Ор	erand I (mask)	Opera	nd 2 (low bound)	12 13	14 15
1525 15	40 1545		1 5370 .			Operand	3 (high bound)
		15,09	* * 2.	. 15 A	1 5 5 5	156	0
OP OFFSE	2 3	4 5		Byte 8 9	10 11		
	TOSCI VEI		Operand I (mag	sk)	<u> </u>	12 13	14 15
1565 15	25 1380	1	,-	-	<u> </u>	perand 2 (compare va	ilue)
		1525	5 F	ig. 15B	1590		
			Diat	osition			
7.	6	5		OSITION			
	OP_CODE		4	3	2		
	1526			BAŞB_RBG		SIZ	0 B <sub>.</sub>
					1530	15 35	•

F19. 15C

	Г			 т				
1590	Onormal	0 x 010203040000	0 x 090a0h0c0h0c	0 × 78020000		0 × 78020000		00 X 0
1585	Operand 1	0 X fffffff0000	0 × fffffff0000	0 × ffff0000		0 × IIII-0000	0 4 7 0	) (
1580	Reserved						-	
1535 1575	OFFSET	0	9	12	4	2	<del></del>	
\$ [	SIZE	⋖	⋖	<b>-</b> J	-	1	മ	-
1530	OP_CODE BASE_REG SIZE OFFSET	DIX_BASE	DIX_BASE	IP_BASE	IP BASE		IP_BASE	
1526	OP_CODE	MASK	MASK	MASK	MASK		MASK	
		_	2	က	4		2	
		1600	1605	1610	1615		1620	1

FIG. 16A

		1526	6 1530		1535 15	1575 1580	1550	1555	1560
		OP_CODE	BASE_REG	SIZE	SIZE OFFSET	Reserved	Operand 1		
1600b	<del>-</del>	BIT	DC BACE	2			ם ביים	Operand 2	Operand 3
			30Kg_0 -	മ	<b>&gt;</b>		Bit MASK 1a	Bit MASK 1b	Bit MASK 1c
(	(	ļ							
1605b	7	811	ULP_BASE		0		0 × 0010000	0	0 × 0000001
1610b	ო	RANGE	PQ_BASE	В	0		0 x e 0	0 × 40	) d × ()
									0
1615b	4	RANGE	ULP_BASE	≥	0		0 × ffff	0 x 151	157
(2)0h									2
	5	RANGE	ULP_BASE	≥	. 2			000	
							= < >	077	720

FIG. 16B

